

« Systems Engineering,  
Today and Tomorrow »

**MAY 18<sup>th</sup>**  
afternoon

## AEIS, Spanish Chapter **PROGRAM**

14:55 PLENARY

Room A

14:55	<ul style="list-style-type: none"> <li>▪ Welcome to AEIS chapter (Agenda / Rules)</li> </ul>	<p><b>José Miguel Ramon Lopez</b> <i>Chairman</i></p>
15:00	<ul style="list-style-type: none"> <li>▪ <a href="#"><u>“Sailing the V with an intelligent compass: engineering digitalization through the automatization of traceability, reuse, and early quality in the development lifecycle”</u></a></li> </ul>	<p><b>Jose FUENTES</b> <i>The Reuse company</i></p>
15:30	<ul style="list-style-type: none"> <li>▪ “Validating system architectures with behavior diagrams”</li> </ul>	<p><b>Becky Petteys</b> <i>Mathworks</i></p>
16:00	<ul style="list-style-type: none"> <li>• “Stay on track with Capella”</li> </ul>	<p><b>Samuel Rochet</b> <i>Obeo</i></p>
16:30	<p>Coffee break</p>	
16:45	<ul style="list-style-type: none"> <li>▪ “Where Systems Engineering meets AI/ML”</li> </ul>	<p><b>Jose Maria Alvarez</b> <i>UC3M</i></p>
17:15	<ul style="list-style-type: none"> <li>▪ “Skydweller implements MBSE to develop first, commercially viable pseudo-satellite”</li> </ul>	<p><b>Georg Hoefler</b> <i>Skydweller</i></p>
17:45	<ul style="list-style-type: none"> <li>▪ AEIS conclusion (10 mn)</li> </ul>	<p><b>José Miguel Ramon Lopez</b> <i>Capgemini</i></p>